

AMENDMENTS TO THE CLAIMS:

1. - 36. (Canceled).

37. (Previously Presented) A network device for use in a network transmitting packets, the device comprising:

a timer;

a processor operable for setting a transmit bit in an outgoing packet and starting the timer when the transmit bit is set, and for reading a receive bit in a received packet and stopping the timer when the receive bit is read.

38. (Previously Presented) The network device in accordance with Claim 37 further comprising:

an interface coupled to the processor, the interface operable for coupling the network device to the network and for transmitting the outgoing packet.

39. (Previously Presented) The network device in accordance with Claim 37 wherein the processor is further operable for reading a set transmit bit in the received packet and for setting a receive bit in another outgoing packet in response to reading the set transmit bit in the received packet.

40. (Previously Presented) The network device in accordance with Claim 37 wherein the outgoing packet and the received packet are voice packets.

41. (Previously Presented) The network device in accordance with Claim 37 further comprising:
a round trip register operable for receiving a value from the timer.

42. (Previously Presented) The network device in accordance with Claim 41 wherein the processor is further operable for comparing the value in the round trip data register to a predetermined value and sending an indication to a user when the value in the round trip data register is greater than the predetermined value.

43. (Previously Presented) A device for use in a network transmitting packets, the device comprising:

a timer;

a transmitting state machine operable for setting a transmit bit in an outgoing packet and starting the timer when the transmit bit is set; and

a receiving state machine operable for reading a receive bit in a received packet and stopping the timer when the receive bit is read.

44. (Previously Presented) The device in accordance with Claim 43 further comprising:

an interface coupled to the transmitting state machine, the interface operable for coupling the device to the network and for transmitting the outgoing packet.

45. (Previously Presented) The device in accordance with Claim 43 wherein the receiving state machine is further operable for reading a set transmit bit in the received packet and for setting a receive bit in another outgoing packet in response to reading the set transmit bit in the received packet.

46. (Previously Presented) The device in accordance with Claim 43 wherein the outgoing packet and the received packet are voice packets.

47. (Previously Presented) The device in accordance with Claim 43 further comprising:
a round trip register operable for receiving a value from the timer.
48. (Previously Presented) The device in accordance with Claim 47 further comprising a processing means operable for comparing the value in the round trip data register to a predetermined value and sending an indication to a user when the value in the round trip data register is greater than the predetermined value.

49. (Previously Presented) A system for use in timing the transmission of voice packets through a network, the system comprising:

a timer;

a processor operable for constructing an outgoing first voice packet, for setting a transmit bit in the outgoing first voice packet, and for starting the timer when the transmit bit is set.

50. (Previously Presented) The system in accordance with Claim 49 further comprising:

an interface coupled to the processor, the interface operable for coupling the system to the network and for transmitting the outgoing first voice packet.

51. (Previously Presented) The system in accordance with Claim 50 wherein the interface is operable for receiving a second voice packet, and the processor is operable for checking the second voice packet to determine if a receive bit is set and stopping the timer if the receive bit is set.

52. (Previously Presented) The system in accordance with Claim 51 further comprising:

a round trip data register operable for receiving a value from the timer.

53. (Previously Presented) The system in accordance with Claim 52 wherein the processor is further operable for comparing the value in the round trip data register to a predetermined value and sending an indication to a user when the value in the round trip data register is greater than the predetermined value.

54. (Original) The system in accordance with Claim 49 wherein the processor is further operable for receiving a second voice packet and checking the second voice packet to determine if the transmit bit is set, and further operable for constructing an outgoing third voice packet and setting a receive bit in the outgoing third voice packet if the transmit bit is set in the received second voice packet.

55. (Previously Presented) A network device for use in a network transmitting packets, the network device comprising:

a link for communicating with external devices over the network, the link comprising,
an interface operable for coupling the link to the network and for transmitting and receiving packets,
a timer, and
a processor coupled to the interface, the processor operable for setting a transmit bit in an outgoing packet and starting the timer when the transmit bit is set, and for reading a receive bit in a received packet and stopping the timer when the receive bit is read.

56. (Previously Presented) The network device in accordance with Claim 55 wherein the processor is further operable for reading a set transmit bit in the received packet and setting a receive bit in another outgoing packet in response to reading the set transmit bit in the received packet.

57. (Previously Presented) The network device in accordance with Claim 56 wherein the outgoing packet, the another outgoing packet and the received packet are voice packets.